

In the drawings:

Please substitute Figure 3C as enclosed in the attached request for drawing change. In the drawing change, Figure 3C is amended so reference number 20 refers to the correct portion of the drawing. No new matter is introduced due to this change.

REMARKS

Reconsideration and allowance of the above-referenced application are respectfully requested. The foregoing amendments are responsive to the April 25, 2001 Office Action. Applicants respectfully request entry of the requested amendments and reconsideration of the application in view of the following comments.

Amendment to the Title

The title has been amended herein to better describe the invention. Applicant respectfully requests entry and approval of the new title.

Response to Drawing Objections

The drawings are objected to because the reference numbers 20 and 21 in Figure 3B both refer to the same element. It appears the objection is actually directed to Figure 3C, and Applicant request a drawing change to Figure 3C correcting the error.

The drawings are further objected to under 37 C.F.R. § 1.83(a) as not showing every feature of the claims. The objection alleges that the feature "at least two source lines formed over said thin film transistor" claimed in Claims 5, 14, 17, and 20 are not shown in the drawings. Applicants respectfully

traverse this rejection. As seen in Figures 2A, 3A, and 3B (see attached Appendix), the source lines 18 in parallel are formed over an active layer 12, or a thin film transistor. Thus, each of the features of the claims are shown in the drawings.

Response to the Rejection based on Double Patenting

Claims 1-4 were rejected under the judicially created doctrine of double patenting over Claims 1, 10, 13, and 16 of co-owned U.S. Patent No. 6,088,070. Applicants request the response to the double patenting rejection be deferred until Applicants complete prosecution of the art rejections. At that time, Applicants will be in a better position to determine the proper disposition of the rejected claims.

Response to Claim Objections

Claims 11-13 are rejected as including a spelling error. Claim 11 is amended herein to correct the error. Claims 5-7 and 14-22 stand rejected under 35 U.S.C. § 112, first paragraph as containing subject matter not described in the specification. As discussed above with reference to the objection to the drawings, the feature of "at least two source lines formed over said thin film transistor" is disclosed and shown in Figures 2A,

3B, and 3C. Thus, Applicants respectfully submit that all claims now comply with the requirements of 35 U.S.C. § 112.

Response to the Claim Rejections Under 35 U.S.C § 102

Claims 8-10 are rejected under 35 U.S.C. § 102(b) as being anticipated by U.S. Patent No. 5,459,596 issued to Ueda, et al. The rejection asserts that Ueda allegedly teaches each element of the claims.

The present invention is directed toward a thin film transistor and a capacitor that are located below the region where disclination occurs. Because the thin film transistor and the capacitor are below the disclination area, the effective aperture area can be increased (see specification, page 14, lines 10-11). Ueda teaches providing a shield electrode so that it overlaps with the four sides of the periphery of the pixel electrode and overlaps with the signal line in order to prevent a display defect due to disclination (see, col. 8, lines 30-32). Ueda never teaches locating the thin film transistor and a capacitor below the region where disclination occurs.

In view of the foregoing distinctions, Applicants respectfully submit that independent Claim 8 is patentably distinguished over the cited art. Applicants respectfully submit

that Claim 8 is in condition for allowance, and Applicants respectfully request allowance of Claim 8.

Claims 9 and 10 depend either directly or indirectly from independent Claim 8. Each dependent claim further defines the independent claim from which it depends. In view of the foregoing remarks regarding independent Claim 8, Applicants respectfully submit that Claims 9-10 are likewise in condition for allowance. Applicants respectfully request allowance of dependent Claims 9-10.

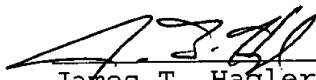
Summary

In view of the above amendments and remarks, all of the claims should be in condition for allowance. A formal notice to that effect is respectfully solicited.

Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

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Version with markings to show changes made

In the claims:

Claim 11 has been amended as follows:

11. (Amended) An active matrix device comprising:
 - a first substrate;
 - a second substrate opposed to said first substrate;
 - a thin film transistor formed over said first substrate, said thin film transistor comprising an active layer;
 - a metal interconnection connected to said active layer;
 - an insulating film formed on said metal interconnection;
 - a light blocking film formed on said insulating film and over said thin film transistor;
 - a capacitor formed between said metal interconnection and said light blocking film with said insulating film interposed therebetween;
 - an organic film comprising an organic material formed on said [Tight] light blocking film;
 - a pixel electrode formed on said organic film and connected with said metal interconnection; and
 - liquid crystal molecules arranged between said first substrate and said second substrate;

wherein a disclination of said liquid crystal molecules is occurred in a region, and wherein said thin film transistor and said capacitor are located below said region.